

Notice of References Cited	Application/Control No. 10/734,340	Applicant(s)/Patent Under Reexamination STOCKMEYER, LARRY J.	
	Examiner Ryan P. Fiegler	Art Unit 2183	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,715,391	02-1998	Jackson et al.	712/11
	B	US-			
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	K	US-			
	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

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	U	Fujii, Tetsuro and Sadayasu Ono. "Interconnection Network Switch Design for Parallel DSP Systems. IEEE: 1992.
	V	Reed, Daniel A. and Richard M. Fujimoto. Multiprocessor Networks: Message-Based Parallel Processing. MIT Press: 1987. Pg 43, 46-52.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.